



Final Exam / 2017-2018

Note1: Answer four questions only.

Note2: Each Question has (25) marks.

Q1// (A) Match the elements in column (A) with the elements of column (B) to complete the meaning:

A	B
1. Perform arithmetic and logic operations and controls the timing.	a. ALU
2. Output Interface	b. capacitor
3. DRAM	c. pipelining
4. Queue	d. CPU
5. Instruction Queue	e. BIU
	f. Latches

(B) What are the meaning of the following abbreviations?

PnP – AGP – CPS – PCI - EEROM

Q2// Compare between the following:

1. Bipolar and MOS Memory.
2. Stack and Queue.
3. Isolated and Memory Mapped I/O.
4. Register and Immediate addressing mode.

Q3// Answer the following:

1. List the advantage(s) of BIU and EU?
2. Mention the reasons behind calling SRAM that it is static and volatile.
3. Give examples for input and output devices (three for each of them)
4. What are the advantages of  $\mu P$ ?
5. JUMP and CALL instructions use three forms of program-memory addressing modes. Mention them.

Q4// (A) Draw the basic i/p interface.

(B) Define the following:

Microprocessor Architecture - Throughput – Editor - System Clock - Accumulator register

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Q5// Explain the functions of the following:

1. Handshaking.
2. Data transfer between a peripheral device and the  $\mu C$ .



رئيس القسم

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*Good Luck*



مدرسة المادة

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